# FACULTY PROFILE

## Dr. Bolem Vamsy Krishna

Department: Electronics and Communication Engineering
College: Gudlavalleru Engineering College
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## Address for Communication

1/115, Sulthanagaram
Gollapalem (Post), Bandar (Mandal),
Krishna (District), Andhra Pradesh, Pin Code: 521001.

## Academic Qualifications

| S. No | Name of the Degree (Starting from Ph.D to 10th Class) | University | Percentage of Marks/Grade | Specialization | Year of Pass/Completed |
|---|---|---|---|---|---|
| 1 | PhD | KL Deemed to be University, Vijayawada | -- | Micro Electronics | 2024 |
| 2 | M.Tech | JNTU Kakinada | 70.85 | VLSISD | 2009 |
| 3 | B.Tech | JNTU Hyderabad | 61.88 | ECE | 2007 |
| 4 | Diploma | State Board of Technical Education | 69.30 | DECE | 2004 |
| 5 | SSC | State Board of Secondary Education | 57.85 | -- | 2001 |

## Professional Experience

| S. No. | Designation | Institution Name | Working Period | |
|---|---|---|---|---|
| | | | **From** | **To** |
| 1 | Assistant Professor | Seshadri Rao Gudlavalleru Engineering College | 07-06-2012 | Till date |
| 2 | Assistant Professor | Usha Rama College of Engineering and Technology | 12-05-2011 | 06-06-2012 |
| 3 | Assistant Professor | Sri Sarathi Institute of Engineering and Technology | 14-05-2007 | 06-05-2011 |

## Professional Body Membership

| S. No | Name of the Professional body | Membership Number |
|---|---|---|
| 1 | IEI | AM156034-7 |

## Papers Published in Journals No. : 12

1. P.Bala Krishna and **B.Vamsi Krishna** "Designing of Embedded Web Server based on Energy Meter" in IJERA journal, Vol.3, Issue 5, September-October 2013
2. Ch. Sri Lakshmi and **B.Vamsi Krishna** "A Novel Method to Improve the Logic Test by Using Single Cycle Access Structure" in IJERA journal, Vol.3, Issue 6, November-December 2013
3. G.Vijayaraju and **B.Vamsi Krishna** "Automatic Screening of Missing Objects and Identification with Group Coding of RF Tags" in IJERA journal, Vol.3, Issue 6, November-December 2013
4. K.Jaswanth Singh, **B.Vamsi Krishna**, "Design and Implementation of Modified Booth Encoder Multiplier using Carry Select Adder", International Journal of Recent Technology and Engineering, 2014.
5. **B.Vamsi Krishna**, M. A. Majeed, Dr.M. Kama Raju, "A Real-Time Remote Control And Monitoring System Using Zynq SoC FPGA Based Web Server" International Journal Of Engineering Sciences & Research, 2016.
6. **B.Vamsi Krishna** "Design and Development of Automated Public Transportation System and Traffic Guidance System" IOSR Journal of Electronics and Communication Engineering (IOSR-JECE) e-ISSN: 2278-2834, p-ISSN:2278-8735.Volume 12, Issue 1, Ver. II (Jan.-Feb. 2017), PP 37-44.

7. Y.Jaya Praksh Reddy, **B.Vamsy Krishna** "Wireless Notice –Board using Node MCU" International Research Journal of Engineering and Technology (IRJET) Journal Volume: 07, issue: 08, August 2020.

8. **B.Vamsi Krsihna**, S.Ravi, M.Durga Prakash, "Recent developments in graphene based field effect transistors" Materials Today: Proceedings, Elsevier https://doi.org/10.1016/j.matpr.2020.07.678 2214-7853/, 2020 Elsevier Ltd. All rights reserved, 21 September 2020.

9. **Bolem Vamsi Krishna**, Shaik Ahmadsaidulu, Surapaneni Sai Tarun Teja, Durga Prakash M "Design and Development of Graphene FET Biosensor for the Detection of SARS-CoV-2", Silicon journal, September 2021.

10. Durga Prakash M, **Bolem Vamsi Krishna,** Satyanarayana V V Bonthu, Shaik Ahmadsaidulu "A Study of an Ultrasensitive Label Free Silicon Nanowire FET Biosensor for Cardiac Troponin I Detection" Silicon journal, September 2021.

11. **B. Vamsy Krsihna,** G. Anith Chowdary, S. Ravi, Kunduru Venkat Reddy, K. R. Kavitha, Asisa Kumar Panigrahy and M. Durga Prakash, "Tunnel Field Effect Transistor Design and Analysis for Biosensing Applications", doi.org/10.1007/s12633-022-01815-3, Silicon, March 2022, Springer.

12. **B. Vamsy Krsihna,** A. Gangadhar, S. Ravi3, D. Mohan, Asisa Kumar Panigrahy, V. Raja Rajeswari and M. Durga Prakash "A Highly Sensitive Graphene-based Field Effect Transistor for the Detection of Myoglobin" https://doi.org/10.1007/s12633-022-01790-9, Silicon, February 2022, Springer.

13. Ch. Amarnatha Sarma, Bolem Vamsi Krishna, K Narasimha Reddy, "Compact Symmetrical Inter digital Filter with Vias and DGS for 802.11b/g/n Applications", Resbee Publishers Journal of Networking and Communication Systems, DOI: 10.46253/jnacs.v7i3.a1, June 2024.

14. Dr. B. Vamsi Krishna, A. Harshitha, Dr. G. Sridevi, Dr. T. Ananda Babu, "Speckle Noise Reduction in Sar Images Using Amosds Optimization with Multidimentional Transfroms" International journal of analytical and experimental modal analysis, ISSN NO: 0886-9367, Volume XVI, Issue 09, September /2024.

## Papers Published in Conferences No. : 3

1. **B.Vamsi Krishna**, M.Durga Prakesh "Dual Gate Junction less Gate-All-Around (JL-GAA) FETs using Hybrid Structured Channels" two day International Conference for Emerging Technology (INCET) Belgaum, India. Jun 5-7, 2020 published in IEEE Explore on 03 August 2020, **DOI:** 10.1109/INCET49848.2020.9154102, **INSPEC Accession Number:** 19887660.

2. B.Vamsy Krishna, Dr vijayalakshmi ch, R Maheswari, Ch.Kavitha, L.Lavanya, Madhavi kemidi, "Analysis of Used Vehicle Value Prediction using Machine Learning", 2024 5th International Conference on Image Processing and Capsule Networks (ICIPCN), DOI 10.1109/ICIPCN63822.2024.00033, 979-8-3503-6717-1/24/$31.00 ©2024 IEEE.

3. B. Vamsy Krishna, Ramkumar Natarajan, Sivamani Chinnaswamy, P. Murugapandiyan, N. Vigneshwari, Nirmala Devi K, "Impact of AlGaN Barrier Composition on Enhancement-Mode GaN HEMTs on Ultra-Wide Band Substrate for RF Applications", 2024 First International Conference on Innovations in Communications, Electrical and Computer Engineering (ICICEC) | 979- 8- 3503- 7651- 7/ 24/$ 31.00 ©2024 IEEE | DOI: 10.1109/ ICICEC62498.2024.1080819

## Workshops Organized No. : 21

1. Faculty Development Program on "VLSI and Embedded System Design" in 2012, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru.
2. One day workshop on "Design and Development of sensor based applications using microcontrollers" for IV B.Tech. Students" organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru in 2013.
3. Organized a workshop on "Blogging", organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 15th October, 2016.
4. Organized a Guest Lecture on "Multirate Signal Processing", organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 21st September, 2016.
5. Organized a Guest Lecture on "Skill Development for young Engineers to reform the core-sector ",organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 15th September, 2016.
6. Organized a workshop on "Intelligent Systems for more Connected World: Internet of Things", organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College with the Technical Support of R & D Team of Efftronics Systems Pvt. Ltd., Vijayawada at Gudlavalleru held during 6th September, 2016.
7. Organized a Guest Lecture on "VLSI & Signal Processing -A View", organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 24th August, 2016.
8. Organized a workshop on "Verilog Programming: Hands on Experience", organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 3rd – 7th August, 2016.
9. Organized a Seminar on "Curriculum Design: Introduction to cognitive Internet of Things", organized by ISF in association with IE and the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 3rd August, 2016.
10. Organized a workshop on "Swarm Intelligence Techniques - Applications to Signal Processing, Communications and Instrumentation", organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held during 5th – 7th February, 2016.

11. Organized a workshop on "Advanced Telecom Technologies", organized by IE in association with the BSNL, Vijayawada and ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 11th – 13th January, 2016.

12. Organized a workshop on "Image processing Applications using MATLAB", organized by ISF in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 12th – 13th October, 2015.

13. Organized a workshop on "Emerging Technology : Internet of Things", organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 21st – 22nd September, 2015.

14. Organized a Guest Lecture on "ALIEN VISION : Exploring Electromagnetic Spectrum with Imaging Technology", organized by IE in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 15th September, 2015.

15. Organized a Guest Lecture on "Future Development of Product/Solution", organized by ECSAt in association with the ECE Dept. of Gudlavalleru Engineering College, at Gudlavalleru held on 5th September, 2015.

16. Organized a Guest Lecture on "Detection and Estimation in SONAR Signal Processing", organized by Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 1st August, 2015.

17. Organized a two day workshop on ROBOTICS for III B.Tech ECE Students with Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 8 to 9th August, 2018.

18. Organized a two day workshop on PCB Design for II B.Tech ECE Students with Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 2nd to 3rd January, 2019.

19. Organized a two day workshop on IOT for III B.Tech ECE Students with Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 4th to 5th January, 2019.

20. Organized a two day workshop on DRONES for III B.Tech ECE Students with Department of ECE, Gudlavalleru Engineering College at Gudlavalleru held on 18th to 19th January, 2019.

21. Organized a one day workshop on "Making of Electronics Circuits" for I B.Tech ECE Students with Department of ECE, Gudlavalleru Engineering College, at Gudlavalleru held on 20th March, 2019.

22. A one week SDP is organized on "VLSI Design Unlocked: A Comprehensive Workshop" for III B. Tech ECE students Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru during 23-26 October 2024.

## Workshops /Conferences /Seminars Attended No. : 34

1. Two day 5th biannual technical Conference on "Signal Processing and Communication Engineering Systems" (SPACES) organized by Department of ECE, K L University held during 4th-5th January 2018 at K L University, Vaddeswaram.

2. Two day 6$^{th}$ International Conference on "Computing, Communication and Sensor Network" held during 30$^{th}$-31$^{st}$ December 2017, organized by Applied Computer Technology in Association with International Association of Science, Technology and Management at Kolkata.

3. Two day FDP on "Advances in VLSI Design and Embedded systems" organized by department of ECE, Gudlavalleru engineering college, during 6$^{th}$ -7$^{th}$ November, 2017.

4. Attended in the "Popular lecture on applications of microwave energy" organized by IE (INDIA), A.P. state centre, Vijayawada on 4$^{th}$ September 2017.

5. Two day international workshop on "Advanced Modeling of Signals and System for Industrial Applications" (AMSSIA-2017) organized by ECE department, KL University during 9$^{th}$-10$^{th}$ January 2017 at KL University, Vaddeswaram.

6. Three day workshop on "Research Methodology" organized by ECE department, KL University during 5$^{th}$-7$^{th}$ January 2017 at KL University, Vaddeswaram.

7. One day workshop on "Embedded System Design for Wireless Sensor Networks Applications" organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 7$^{th}$ January 2015.

8. One day workshop on "Speech Processing: Current Challenges and Hands-on Experience" organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 23$^{rd}$ August 2014.

9. One week faculty refresher course on "Signals & Systems" organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 9$^{th}$ – 13$^{th}$ June 2014.

10. Two day workshop on "Embedded System Development", organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 30$^{th}$ - 31$^{st}$ December 2013.

11. Two day workshop on "Issues in Integrated Circuits (ICs) Design" organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 13$^{th}$ – 14$^{th}$ December 2013.

12. One day workshop on "Ultra low Power Communication using CC430 Microcontroller: Hands on Experience" on 7$^{th}$ February 2015 organized by Signal Processing and Communication Research group, Department of ECE, Gudlavalleru Engineering College, Gudlavalleru.

13. One week faculty refresher course on "Electronic Devices and Circuits" organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 23$^{rd}$ – 28$^{th}$ September, 2013.

14. Two day workshop on "Microwave Engineering Concepts and Lab Practice" on 9$^{th}$ to 10$^{th}$ June 2012 organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru.

15. One day workshop on "Research in Energy Management Wireless Networks and Smart Transportation Systems" organized by Institution of Engineers, Vijayawada local center, Vijayawada on 29$^{th}$ March 15$^{th}$ 2014.

16. Two day Faculty development program on "VLSI and Embedded System Design" organized by Department of ECE in association with IETE Vijayawada local center, Vijayawada, during 7-8-July 2012.

17. A two day seminar 32$^{nd}$ National convention of Electronics and Telecommunication Engineering on "Intelligent system for more connected world Internet of Things" organized by Institution of Engineers India Vijayawada state center Vijayawada during 24-25$^{th}$ October 2016.

18. Five day workshop on "Signal Processing Applications" during 7$^{th}$- 12$^{th}$ November 2016 organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru.

19. A one week faculty development on Machine Learning Applications in Signal Processing organized by E & ICT academy, NIT Warangal and ECE department , Gudlavalleru Engineering College during 4$^{th}$ to 9$^{th}$ February 2019.

20. A Six day Faculty development programme on "Emerging Trends in VLSI Design and Technology with Hands on Training from 28$^{th}$ January to 2$^{nd}$ February 2019 under AICTE Margdarshan scheme organized by ECE department VR Siddhartha Engineering College.

21. One week Faculty development programme on "DNA of Mixed Signal IC Design for Portable Systems" organized by the E&ICT academy at department of Electronics and Communication Engineering, NIT Warangal from 20$^{th}$ -25$^{th}$ May 2019.

22. Five day workshop organizes by Apply Volt on "Industrial IOT" 19$^{th}$ to 23$^{rd}$ May 2020.

23. Two day FDP program conducted by Kallam Harinada Reddy Institute of Technology in association with IETE Vijayawada on "Microstrip Antennas for Advanced wireless Communications" during 15$^{th}$ to 16$^{th}$ May 2020.

24. Five day FDP program conducted by St. Martin's Engineering College on "RECENT COMMUNICATION TOOLS AND ITS APPLICATION" during 19$^{th}$ to 23$^{rd}$ May 2020.

25. A two day workshop on "MACHINE LEARNING: CONCEPTS AND APPLICATIONS" organized by Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru during 30$^{th}$ June-1$^{st}$ July 2022.

26. A five day DST-SERB sponsored FDP on "New Opportunities for Emerging 2D Materials in Bioelectronics and Biosensors" from 11-09-2023 to 15-09-2023 organized by Department of ECE, CMR Institute of Technology, Bangalore, INDIA.

27. One Week online FDP on "Quantum Signal Processing", organized by Department of Electronics & Communication Engineering, Lakireddy Bali Reddy College of Engineering, Mylavaram, in Association with IETE, Vijayawada center held during 14.12.2023 to 20.12.2023.

28. One Week Online FDP on "IMPACT OF AI: ROBOTICS, VLSI AND MEDICAL DOMAINS" organized by the Department of ECE, Vasireddy Venkatadri Institute of Technology in association with IETE Vijayawada Centre from 22$^{nd}$ April to 27$^{th}$ April 2024.

29. Attended online national webinar on "Implementation of Adaptive filter using Distributed Athematic Architecture" organized by Department of ECE, KL Deemed to be University, held on 30-03-2024.

30. A five day FDP on "Deep Dive in to IOT Technologies and Applications" organized by Department of ECE, Lakireddy Bali Reddy College of Engineering, Mylavaram from 24-06-2024 to 28-06-2024.

31. A three-day online FDP on "Generative AI" organized by the Department of Information Technology of Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 26 JUN 2024 to 28 JUN 2024.

32. A five day international workshop on "Recent Innovations and challenges in VLSI Design: from concept to Application" organized by Department of ECE, KL Deemed to be University from 18-11-2024 to 22-11-2024.

33. One week online FDP on "Semiconductor Devices, Modeling & Circuit Technology" organized by Department of ECE, Lendi institute of Engineering and Technology, AP during 19-24 August 2024.

34. A one week FDP "IoT and Embedded Systems" organized by Department of ECE, Prasad V. Potluri Siddhartha Institute of Technology, Kanuru, Vijayawada in association with National Institute of Technical Teachers Training and Research during 23/09/2024 to 27/09/2024.

## Certifications/Training Programs Attended No. : 13

1. Two week ISTE STTP on "Introduction to Design of Algorithms" conducted by Indian Institute of Technology Kharagpur from 27$^{th}$ April to 30$^{th}$ May 2015 (This workshop was held under the national mission on education through ICT (MHRD).

**2.** One week training program on "Training Teachers for Excellence", organized by Gudlavalleru Engineering College, Gudlavalleru on 17$^{th}$ to 21$^{st}$ June 2013.

3. Two week ISTE STTP on "Signal and Systems" conducted by Indian Institute of Technology Kharagpur from 2$^{nd}$ to 12$^{th}$ January 2014 (This workshop was held under the national mission on education through ICT (MHRD).

4. Two week ISTE STTP on "Control Systems" conducted by Indian Institute of Technology Kharagpur from 2$^{nd}$ to 12$^{th}$ December 2014 (This workshop was held under the national mission on education through ICT (MHRD).

5. One week refresher course on "Microprocessors and Microcontrollers" from 17/11/12 to 21/11/15 organized by research and development center of Gudlavalleru Engineering College.

6. One day training program on "MATLAB, Simulink and Related Tool Box for Engineering Education" organized by Academy of Design and Architecture in association with Department of ECE, Gudlavalleru Engineering College.

**7.** Two week sort term training program on "Transform Techniques in Signal Processing" from 8$^{th}$ June to 2oth June 2009 organized by department of ECE Lakireddy Balireddy college of Engineering and sponsored by AICTE.

8. Two day staff training program on " Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications" using NI Multisim organized by Department of

Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during $4^{th}$ to $5^{th}$ November 2016.

9. One week national level FDP on "Signal and image processing using Lab View" held during $13^{th} - 18^{th}$ November, 2017 at Gudlavalleru engineering college, organized by Signal processing research group of department of ECE in association with NI systems Pvt. Ltd.

10. Two week faculty development program on "Digital VLSI Circuit Design" held during $3^{rd}$ to $12^{th}$ June 2017 jointly organized by NITW and ICT Academies at NIT Warangal.

11. One week faculty development program on "Communication s and Signal Processing" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during $29^{th}$ 0ctober to $3^{rd}$ November 2018.

12. Two day training program on "Design and Development of Digital Circuits using VLSI Mentor Graphic Tools" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during $6^{th}$ to $7^{th}$ November 2018.

13. AICTE sponsored Three week FDP on "Design Block Chain Technology" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during $10^{th}$ to $29^{th}$ August 2020.

**Online Webinars: 9**

1) Attended an Online Webinar on "Radio Frequency Measurements for Cellular and Wireless Communication Systems" organized by Department of Electronics and Communication Engineering on 25th May, 2020.

2) Attended on Online Webinar on "Impact of COVID in Engineering Education & Way forward", organized by ISTE Chapter, Gudlavalleru Engineering College, Gudlavalleru on 09-06-2020.

3) Attended an Online Webinar on "Crypto currencies and Block Chain", organized by Chalapati Institute of Engineering and Technology on 19-05-2020.

4) Attended an Online Webinar on "Advanced IoT Applications - A Practical Approach" organized by Laki Reddy Bali Reddy College of Engineering $23^{rd}$ May 2020.

5) Attended five day FDP on "Nano Furtherance of Pharmaceutical Engineering: An Industrial Perceive" conducted by Nirmala College of Pharmacy in Association with ATAL Academy during $21^{st}$ to $25^{th}$ June 2021.

6) Attended five day FDP on "Artificial Intelligence and its Applications" conducted by Tirumala Engineering College during $28^{th}$ May 2021 to $1^{st}$ June 2021.

7) Attended one day webinar on "Embedded System Design flow on Zynq using Xilinx vivado and Vitis" conducted by CorelEL Technology in association with Xilinx on $10^{th}$ June 2021.

8) Attended five day FDP on "VLSI circuits and MEMS device modeling: Recent Trends and Advancements" conducted by VR Sidhardha Engineering college in Association with AICTE during $26^{th}$ to $30^{th}$ April 2021.

9) Attended five day FDP on "Bio MEMS and LAB ON CHIP technology for point of care Applications" conducted by C Hakeem College of Engineering and Technology in Association with ATAL Academy during $5^{th}$ to $9^{th}$ July 2021.

# Guest Lectures Delivered No. : Nil

# Subjects Handled

**Under Graduation**

- Microprocessor and Microcontroller
- VLSI Design
- Electronic Measurements and Instrumentation
- Optical Communication
- Digital IC Applications
- Analog Communication
- Structured Digital Design
- Switching Theory and Logic Design
- Analog Electronics
- Elements of Electronic Engineering
- Digital design Through Verilog

**Post Graduation**

- VLSI System Design
- Advanced Microcontrollers

# Other Responsibilities

**College Level**

1. GEC FEST '13 , GEC FEST '14, GEC FEST '15, GEC FEST '16,
2. One week space walk co-cordinator

**Department Level**

1. Time Table Coordinator
2. Autonomous Co-cordinator
3. ECSAt Faculty Advisor
4. NBA Criteria-3 Member
5. Class In-Charge
6. Industrial Tour Coordinator
7. SDP coordinator
8. NBA co-cordinator
9. Student Counseling

# Projects Guided:

## a) Under Graduation

| S. No | Name of the project | Year |
|-------|---------------------|------|
| 1 | Smart Hand Glove | 2022 |
| 2 | Smart Crop Protection from Violent Weather and Intruders | 2021 |
| 3 | COVID-19 Suspect Detection and Alerting System | 2021 |
| 4 | Baby Monitoring System using IOT | 2020 |
| 5 | Wireless Notice –Board using Node MCU | 2020 |
| 6 | Motion Detection Using IR Sensor and Counters | 2016 |
| 7 | Design and Development of Automated Public Transportation System | 2015 |
| 8 | RFID and GSM Based Intelligent Courier Mail Box with Automatic Delivery Notification | 2014 |

## b) Post Graduation

| S.No | Name of the project | Year |
|------|---------------------|------|
| 1 | A Novel Method To Improve The Logic Test By Using Single Cycle Access Structure | 2013 |
| 2 | Automatic Screening of Missing Objects and Identification with Group Coding of RF Tags | 2013 |
| 3 | Designing of Embedded Web Server based on Energy meter | 2013 |
| 4 | Design and Implementation of Modified Booth Encoder Multiplier using Carry Select Adder | 2014 |
| 5 | "A Real-Time Remote Control And Monitoring System Using Zynq SoC FPGA Based Web Server" | 2016 |

## Guiding Ph. D Students

| University | Year of Registration | Area | Status |
|---|---|---|---|
| -- | -- | -- | -- |
| -- | -- | -- | -- |

## Books Published No. : 2
1. Co-author of book entitled "Electronics and Communication Engineering Instructional Material" with ISBN – 978-1- 4276-5539- 4.
2. Co-author of book entitled "Electronics and Communication Engineering laboratory Manual" with ISBN – 978-1- 4276-5536- 3.

## R&D and Consultancy

| S. No | Project Title | Source of Funding | Duration | Role | Status |
|---|---|---|---|---|---|
| 1 | Design and Development of Automated Public Transportation System and Traffic Guidance System. | UGC | 2 years | Hardware and Software Development | Completed |
| 2 | Design of Wireless Intelligence Traffic Guidance System. | In House R & D ECE, GEC | 1 Year | Hardware and Software Development | Completed |

## Patent Published:
1. A patent titled "PREVENTING VEHICLE ACCIDENTS: INTELLIGENT ACCIDENT AVOIDANCE USING SATELLITE-BASED POSITIONING SYSTEM" submitted to Intellectual Property India, Government of India on 07-07-2020 with application No. 202041028920 and published on 31/07/2020.
2. A patent titled "RED DEER ALGORITHM FOR ANOMALY DETECTION IN SENSOR-BASED CYBER-PHYSICAL SYSTEMS" submitted to Intellectual Property India, Government of India on 16/04/2021 with Application No.202141017669 and published on 23/04/2021 and Patent Office Journal No. 17/2021.
3. A patent titled "IOT BASED SMART HOME MODEL FOR ASSISTING ELDERLY PEOPLE" submitted to Intellectual Property India, Government of India on 29/06/2022 with Application No. 202211037454 and published on 8/07/2022.

4. A patent titled "Integrated Alert System with Wireless Network and Method Thereof" submitted to Intellectual Property India, Government of India on 8/09/2022 with Application No. 202241051267 and published on 16/09/2022.

## Curriculum Design and Development
1. BOS member for autonomous course structure and syllabus preparation
2. Optical Communication Course module
3. Electronic Measurements and Instrumentation Course module
3. Microprocessor and Microcontroller Lab Manual

## Academic Rewards / Achievements: Nil
## Extra Curricular Activities: Nil

## Declaration
I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 05-02-2025
Place: Gudlavalleru

**(Dr. B. Vamsy Krishna)**
Signature